

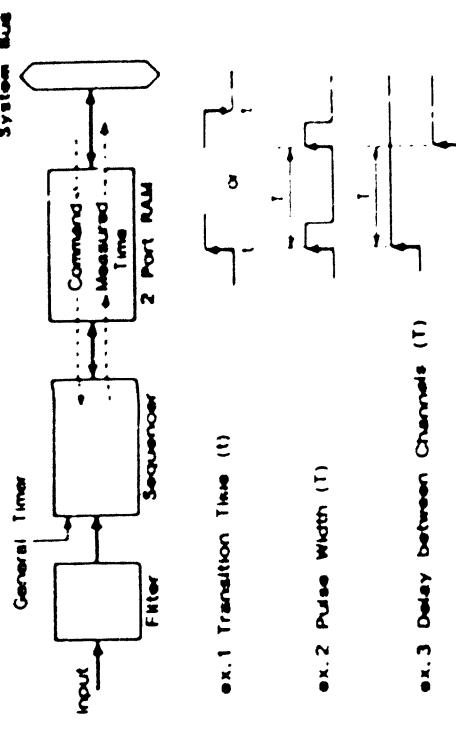
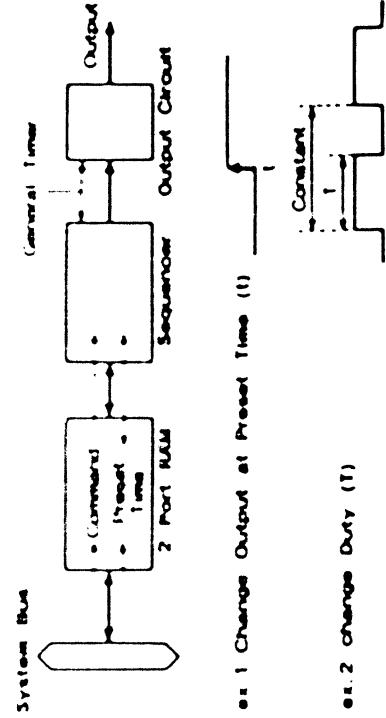
has been added for variable interrupt, it can process the frequently used interrupt. While using hardware, multiplexing and having processor speed of 12 become approximately three times as fast in a microprocessor at the same clock frequency.

#### Fast Timed Input Port (FTI)

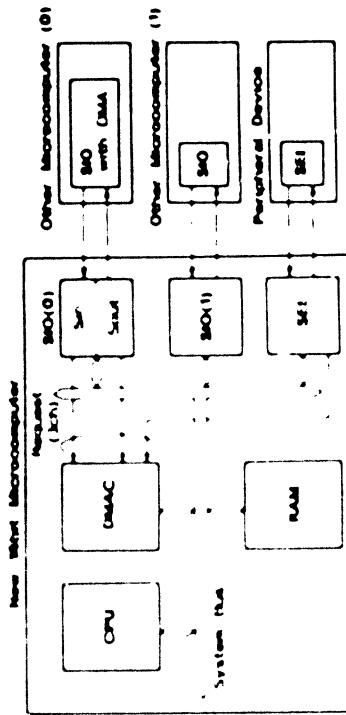
FTI is a highly functional and intelligent input port, designed to reduce the load on the CPU during high speed pulse input. All that is necessary with FTI is to write a command into the control memory; a search is then made for a leading or trailing edge on the designated channel, and the time is measured. It is further possible to measure pulse width, and frequency or phase difference. A block diagram and examples of input are shown in Fig. 3.

FTI is a function that is indispensable for the signal processing of such parameters as engine speed and vehicle speed. It reduces CPU interrupt processing and this enables an increase in precision.

Fast Timed Input Port (FTI) and the RAM A block diagram of memory access transfer between the memory, CPU and the RAM A block diagram is shown in Fig. 4.

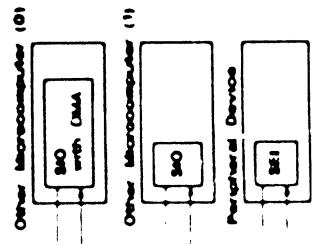


**Fig. 3 Block Diagram of FTI and Examples of Measured Time**



**Fig. 4 Block Diagram of FTO and Examples of Output Pulse**

Fast Timed Output Port (FTO) and the RAM A block diagram of memory access transfer between the memory, CPU and the RAM A block diagram is shown in Fig. 5.



**Fig. 5 Block Diagram of FTO and Examples of RAM Access Transfer**