

Fig. 9 - CPU block diagram

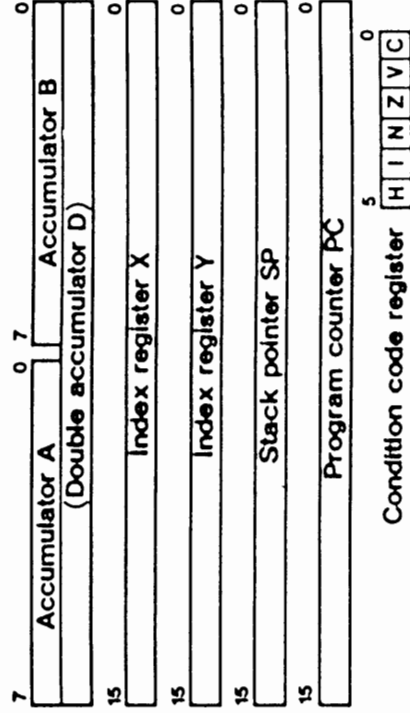


Fig. 10 - Internal registers

There are nine addressing modes, which are factors accessible to the memory:

- IMMEDIATE
- INDEXED
- RELATIVE
- INDIRECT
- DIRECT FOR ACCESS TO ADDRESSES 0-255
- EXTENDED FOR ACCESS TO 64K bytes
- INHERENT WITHOUT OPERAND
- MEMORY IMMEDIATE FOR DIRECT OPERATION TO ADDRESSES 0-255
- BIT MANIPULATION TO ADDRESSES 32-47 AND 64-79

Internal Memory and Memory Connection Function

Memory capacity is an 8K-byte user ROM, a 256-byte RAM (of which 32/64 bytes may be used as